

# **Freescale Communicator** Introducing Freescale's K1xD/K2xD, 50 MHz MCUs

Expanding the Kinetis Universe Communicator Finalization Date: June 12 2012



### **Product Summary**

**On June 13<sup>th</sup> 2012** Freescale announces further expansion of the Kinetis K families with low pin count, mid embedded memory densities MCUs, with **availability starting Aug 3<sup>rd</sup> 2012**. K1xD and K2xD MCUs will offer:

- 50 MHz ARM Cortex-M4 with DSP and MAC
- Mid/high embedded memory densities
  - Up to 512 KB Flash on non FlexMemory devices
  - Up to 64 KB FkexNVM and 4 KB FlexRAM with up to 4KB enhance EEPROM capability
  - o 64 KB SRAM
- Security and Integrity Modules
  - o Hardware Crytptographic Acceleration Unit
  - o Hardware Random Number Generator
  - o Hardware Tamper Detection Unit

#### **Targeted Applications**

- Industrial
- o Lighting
- o Access control
- o Electricity meter
- o Water meter
- o Gas meter
- o Smart grid
- Small appliance household
- o Test and measurement
- ePOS\_EFT
- Consumer
  - o Electronic games
  - o Remote controls
  - o PC peripherals
  - o PND personal navigation
  - o Mobile accesories

#### **Development Tools Summary**

- Support from worldwide ARM ecosystem
- Tower modules for rapid prototyping
- Freescale complimentary CodeWarrior and Processor Expert
- Freescale complimentary MQX RTOS

### **Product Overview**

34 new Cortex-M4 based microcontrollers that offer mid/high embedded memory densities ranging from 192 KB to 512 KB of flash and optional 64 KB of FlexNVM in low pin count with reduced package sizes, from 48-pin LQFP to 81-pin MAPBGA. These MCUs are further optimized for cost-sensitive applications requiring low-power and processing efficiency with optional Tamper Detect and Security features, and expand the most scalable portfolio of low power devices while keeping full software, hardware and development tools compatibility.

#### Product Specifications of the K1xD/K2xD

	Memory					Feature Options										Packages					
																FM	FT	LF	LH	LK	MC
Part Number	CPU (MHz)	Flash (KB)	FlexNVM (KB)	SRAM (KB)	CACHE (KB)	FPU	MPU	CAN	sDHC	Nand Flash controller	External Bus Interface	12-bit DAC	PGA	5V tolerant I/O	Other	32 QFN	48 QFN	48 LQFP	64 LQFP	80 LQFP	121 MAPBGA
MK11DX128Vyy5(R)	50 MHz	128KB	64KB	32KB	-	-	-	-	-	-	-	1	-	-	HW encryption and tamper detect					$\checkmark$	$\checkmark$
MK11DX256Vyy5(R)	50 MHz	256KB	64KB	32KB	-	-	-	-	-	-	-	1	-	-	HW encryption and tamper detect					$\checkmark$	$\checkmark$
MK11DN512Vyy5(R)	50 MHz	512KB	-	64KB	-	-	-	-	-	-	-	1	-	-	HW encryption and tamper detect					$\checkmark$	$\checkmark$
MK12DX128Vyy5(R)	50 MHz	128KB	64KB	32KB	-	-	-	-	-	-	-	1	-	-	-			$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
MK12DX256Vyy5(R)	50 MHz	256KB	64KB	32KB	-	-	-	-	-	-	-	1	-	-	-			$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
MK12DN512Vyy5(R)	50 MHz	512KB	-	64KB	-	-	-	-	-	-	-	1	-	-	-				$\checkmark$	$\checkmark$	$\checkmark$
MK21DX128Vyy5(R)	50	128KB	64KB	32KB								1			HW encryption and Tamper Detec					$\checkmark$	$\checkmark$
MK21DX256Vyy5(R)	50	256KB	64KB	32KB								1			HW encryption and Tamper Detec					$\checkmark$	$\checkmark$
MK21DN512VLK5(R)	50	512KB	-	64KB								1			HW encryption and Tamper Detec					$\checkmark$	$\checkmark$
MK22DX128Vyy5(R)	50	128KB	64KB	32KB								1			USB OTG (FS)			$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
MK22DX256Vyy5(R)	50	256KB	64KB	32KB								1			USB OTG (FS)			$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
MK22DN512VLH5(R)	50	512KB	-	64KB								1			USB OTG (FS)				$\checkmark$	$\checkmark$	$\checkmark$

\*48 LQFP packages do not have 12-bit DAC

#### Key Features and Benefits of K1xD/K2xD MCUs

	Feature	Application Benefit				
Performance	ARM Cortex-M4 with DSP instructions	Up to 50 MHz core supporting a broad range of processing bandwidth needs				
	Up to 16-channel DMA	Peripheral and memory servicing with reduced CPU loading				
Memory and memory interfaces	Up to 512 KB Flash in non- FlexMemory devices	High reliability, fast access program memory. Independent flash banks allow concurrent code and firmware execution				
	64 KB FlexMemory	FlexMem provides 4 KB of user-segmentable byte write/erase EEPROM. In addition, up to 64 KB can be used for extra program code.				
Low-power	10 low-power modes with flash programming and analog operation	Peripheral activity and wakeup times can be optimized to suite application requirements				

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	down to 1.71V	enabling extended battery life					
	Low-power timer, low-power RTC, low-leakage wakeup unit	Continual device operation in reduced power states with flexible wakeup options					
Analog	High-speed 16-bit ADCs	Fast, accurate signal conditioning capability with support for single or differential operation for improved noise rejection					
	12-bit DAC, high-speed comparators	Analog signal generation for audio applications					
	On-chip voltage reference	Eliminates need for external voltage reference reducing overall system cost					
Communications	Up to 4 UARTs with IrDA support	Variety of data size, format and transmission/reception settings supported for multiple industrial communication protocols					
Interface	IIS, up to 2 SPI and up to 2 IIC interfaces	Multiple communication interfaces for simple and efficient data exchange, industrial network bridging and audio system interfacing					
	Cryptographic Acceleration Unit (CAU)	Secure data transfer and storage. Faster than software implementations and with minimal CPU loading. Supports a wide variety of algorithms: DES, 3DES, AES, MD5, SHA-1, SHA-256					
Security and Reliability	Tamper Detection Unit	Secure key storage with internal/external tamper detect for unsecure flash, temperature/clock/supply voltage variations and physical attack					
	Random Number Generator	Facilitates generation of FIPS 140 certifiable random numbers. Reduces CPU loading for cryptographic functions					

## Freescale Support Ecosystem

#### **Development Tools**

#### • TWR-K21D50M

- o K21DN512in 81 MAPBGA
- o Integrated, Open-Source JTAG
- Standalone USB 2.0 On-The-Go (full speed)
- MMA8451Q 3-axis accelerometer
- Tamper pin header
- Tower Plug-In (TWRPI)
- Tower connectivity for access to UART, SPI, I<sup>2</sup>C
- o Plus: Potentiometer, 4 LEDs, pushbuttons, and battery holder
- Available August 2012

#### Software

- $_{\odot}$  Complimentary Codewarrior IDE and Processor Expert
  - CW 10.2 for Kinetis K1xD/K2xD 50 MHz Service pack: alpha version now, production version end July 2012.
  - Processor expert for Kinetis K1xD/K2xD 50 MHz: available October 2012
- o USB PDHC Stack available July 2012
- o Kinetis bootloaders: Available Now
  - o AN4367, AN4368, AN4370, and AN4379.

#### **Operating Systems**

- Software
- Complimentary MQX RTOS
  - MQX v3.8 support for Kinetis K1xD/K2xD
  - Available end July 2012

# Third-Party Vendors Support Ecosystem

#### **Development Tools**

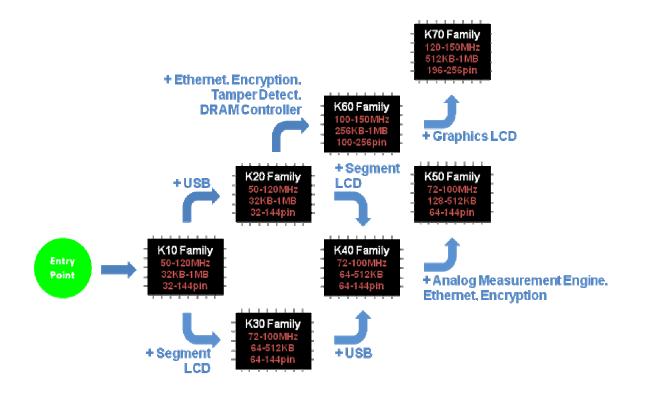
- Software
- $\circ \, \text{IAR}$
- $\circ$  Segger
- o Keil/ARM
- Tools/Probes/STAGs/JTAGs
- $\circ$  P&E

#### **Operating Systems**

- Software
- $\circ \, \text{Micrium}$
- $\circ$  FreeRTOS
- $_{\odot}$  Express Logic

# **Migration Information**

The following graphic illustrates the migration path between all Kinetis families.



# **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Freescale Semiconductor: <u>PK21DN512VMC5</u> PK21DX256VMC5